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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Peter J. Geiss, et al

Examiner: Nema O. Berezny

Serial No.: 10/695,336

Art Unit: 2813

Filed: 10/28/2003

Confirmation No.: 4852

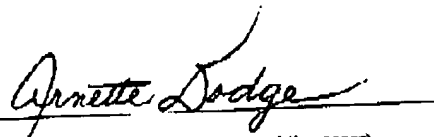
Title: Method Of Controlling Grain Size
In A Polysilicon Layer And In Semi-
conductor Devices Having Polysilicon
StructuresResponse to Election Of Species Requirement

In response to the Office Action dated Jan. 11, 2005, Applicants elect, without traverse, a semiconductor bipolar device (claims 20-26) for continued prosecution in the above-identified patent application.

CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8

I certify that this correspondence of 2 pages is being transmitted by facsimile to the United States Patent and Trademark Office to: Examiner Berezny, Art Unit 2813 at facsimile number 703 872-9306 on Feb. 7, 2005 by Arnette

Dodge.


(Signature of person transmitting paper)

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In view of the above, consideration and allowance are, therefore, respectfully solicited.


In the event the Examiner believes an interview might serve to advance the prosecution of this application in any way, the undersigned attorney is available at the telephone number noted below.

The Director is hereby authorized to charge any fees, or credit any overpayment associated with this communication, including any extension fees, to Deposit Account No. 09-0456.

Respectfully submitted,

For: Peter J. Geiss et al

By:


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Dated: February 7, 2005